

FIG. 1a

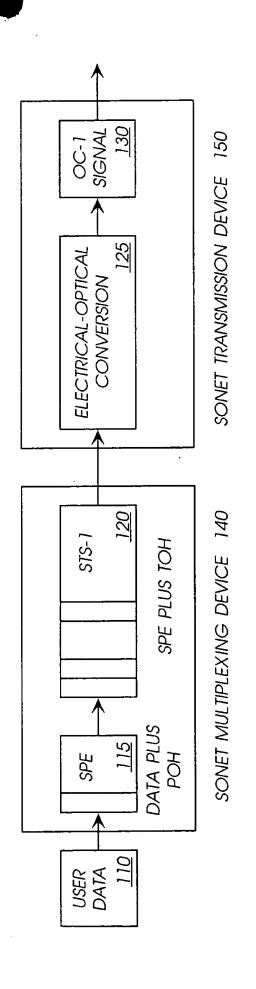


FIG. 1b

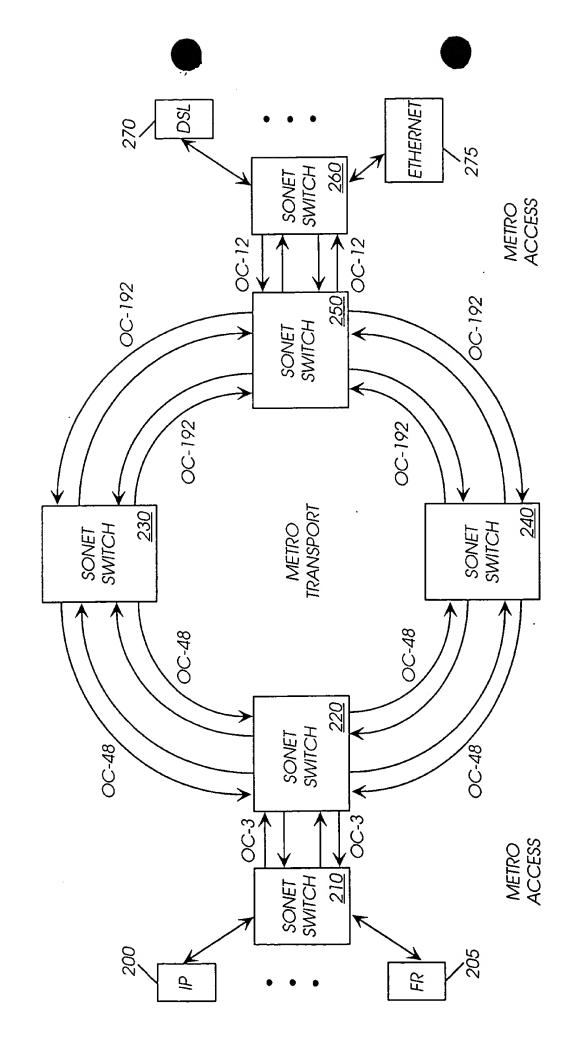


FIG. 2

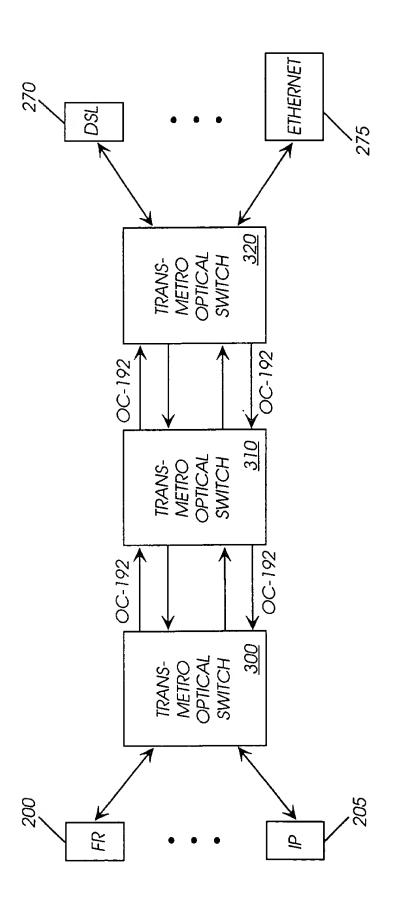
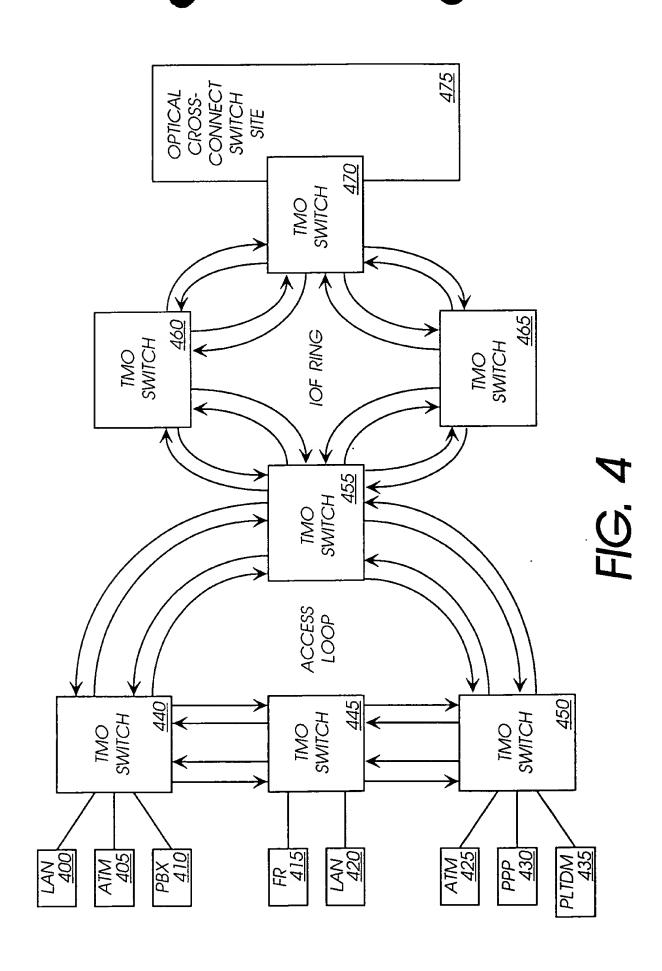


FIG. 3



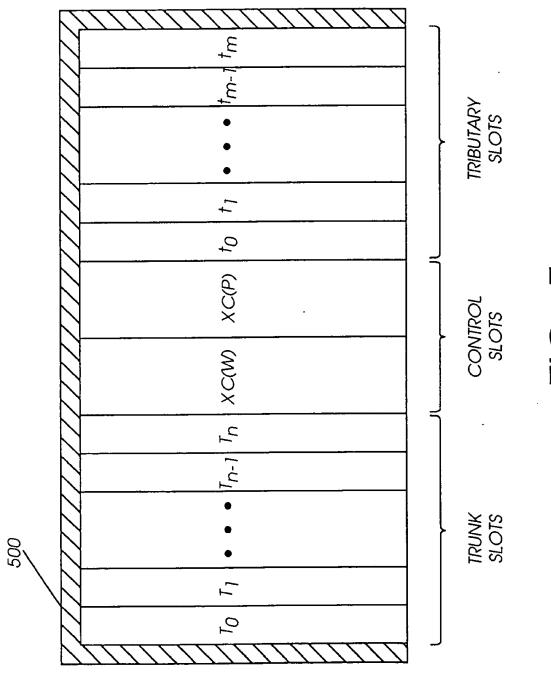
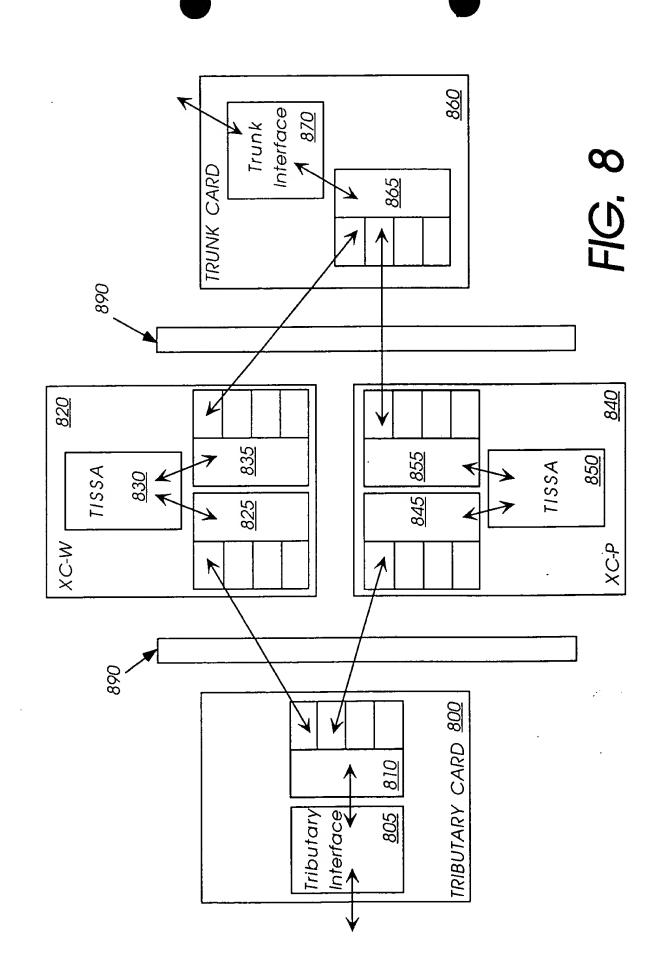


FIG. 5

Deelosz. "Cettoo

F16. 6



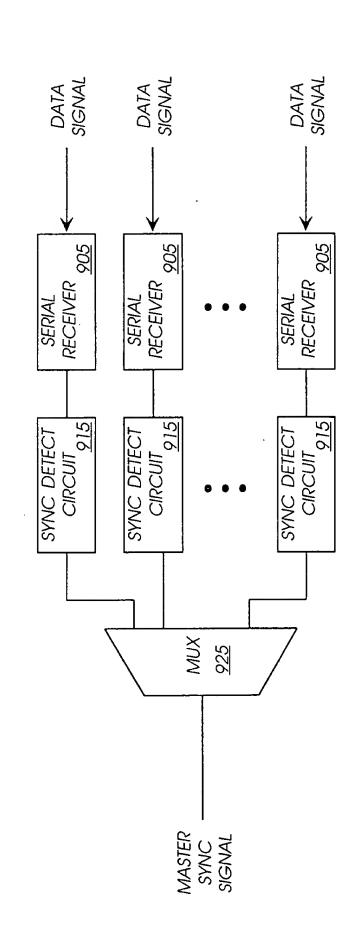
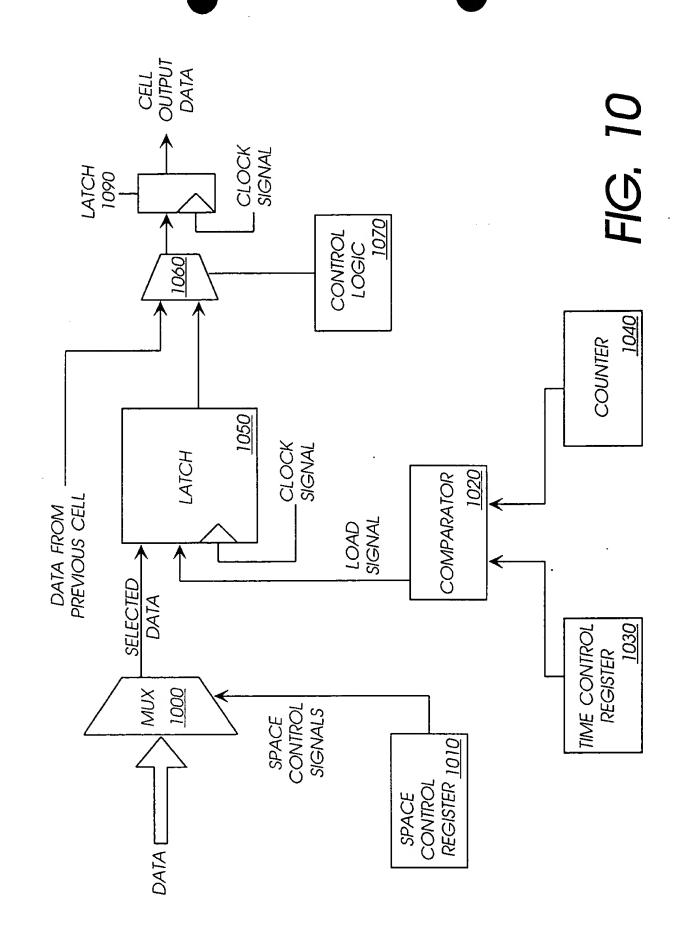
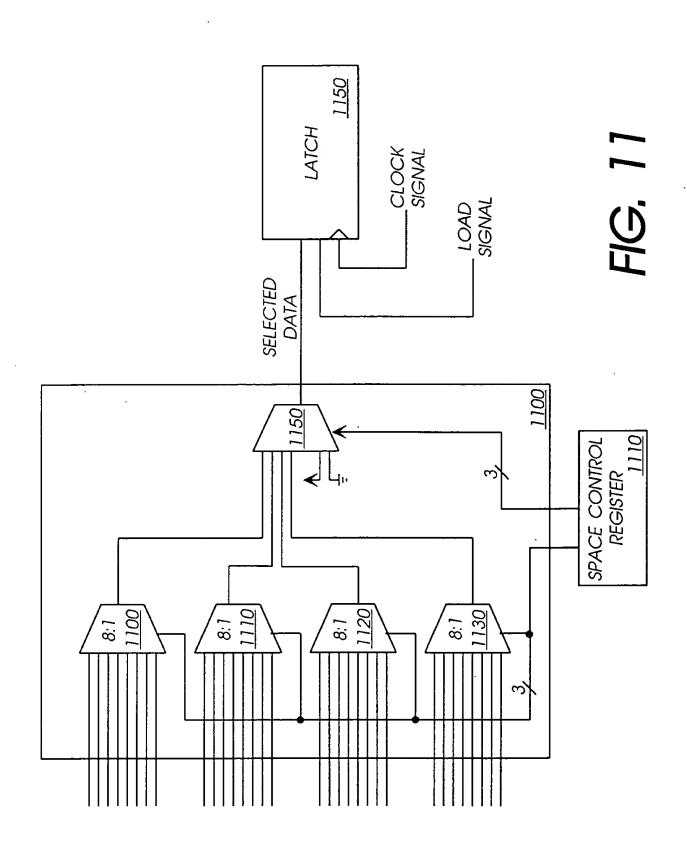
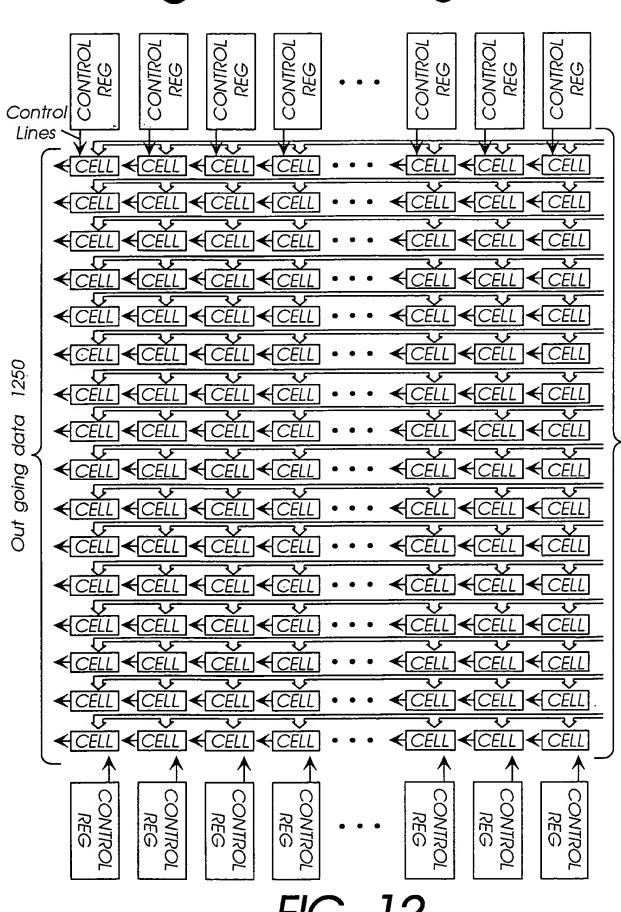


FIG. 9a

										96
	915	020	935	940	955	096	975	086	066	FIG.
	I_{X}	R_X	I_{X}	R_{X}	I_X	R_{X}	T_X	R_{X}		
:		<u>910</u>		<u>930</u>		<u>950</u>		026		
	PARALLEL/SERIAL CONVERSION CIRCUIT	GROUP 0	PARALLEL/SERIAL CONVERSION CIRCUIT	GROUP 1	PARALLEL/SERIAL CONVERSION CIRCUIT	GROUP 2	PARALLEL/SERIAL CONVERSION CIRCUIT	GROUP 3	CONTROL INTERFACE	•
006	1 2	\ \ '	91	\ \	16		91	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		_







1200

In coming data

FIG. 12

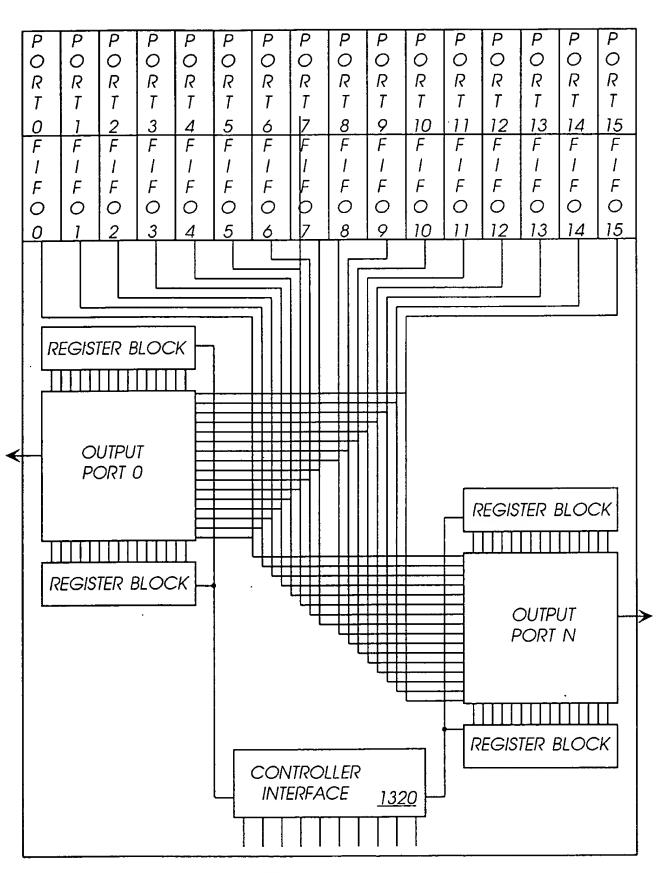


FIG. 13

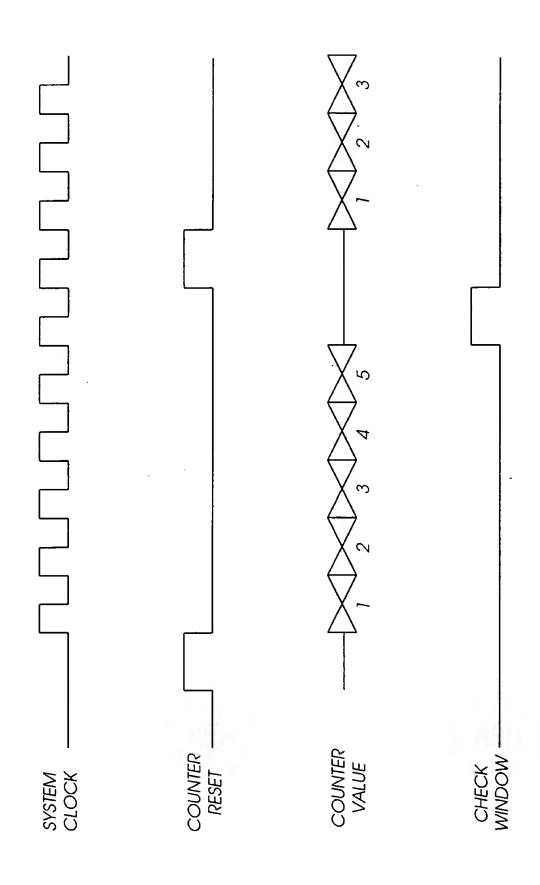


FIG. 14

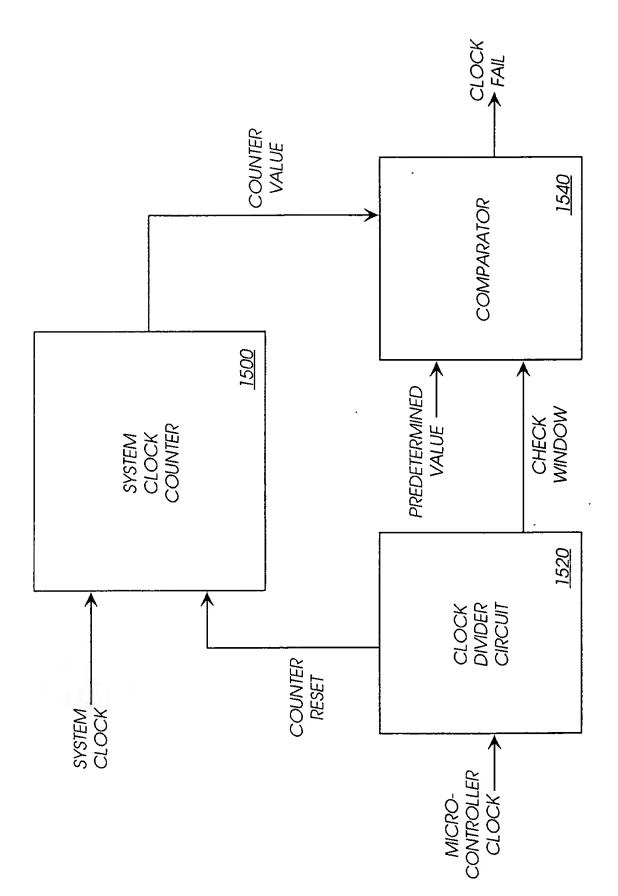


FIG. 15

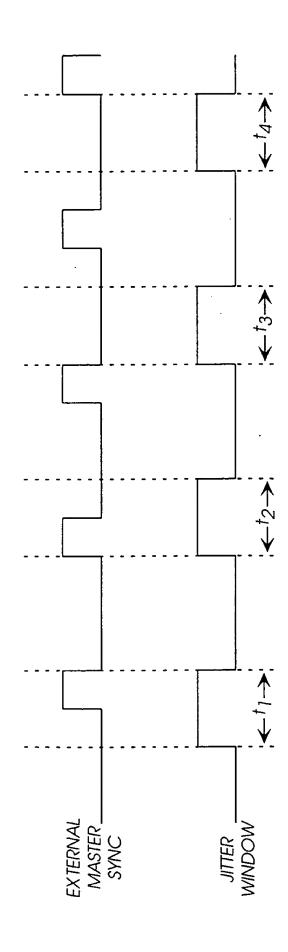
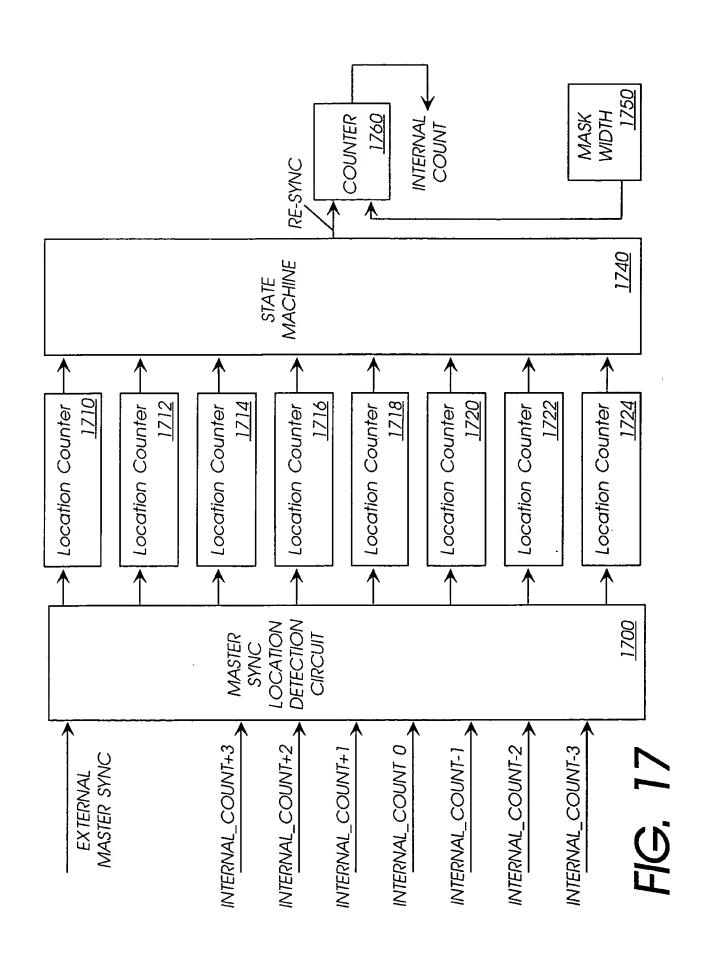


FIG. 16



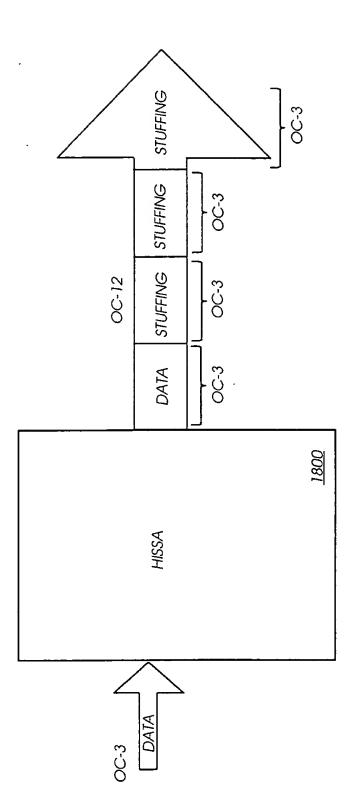


FIG. 18

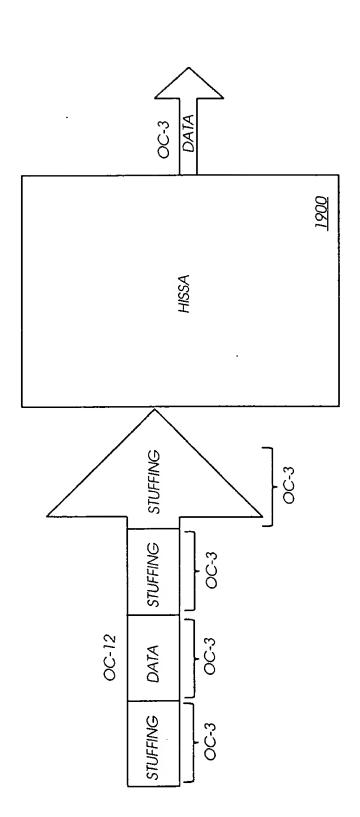
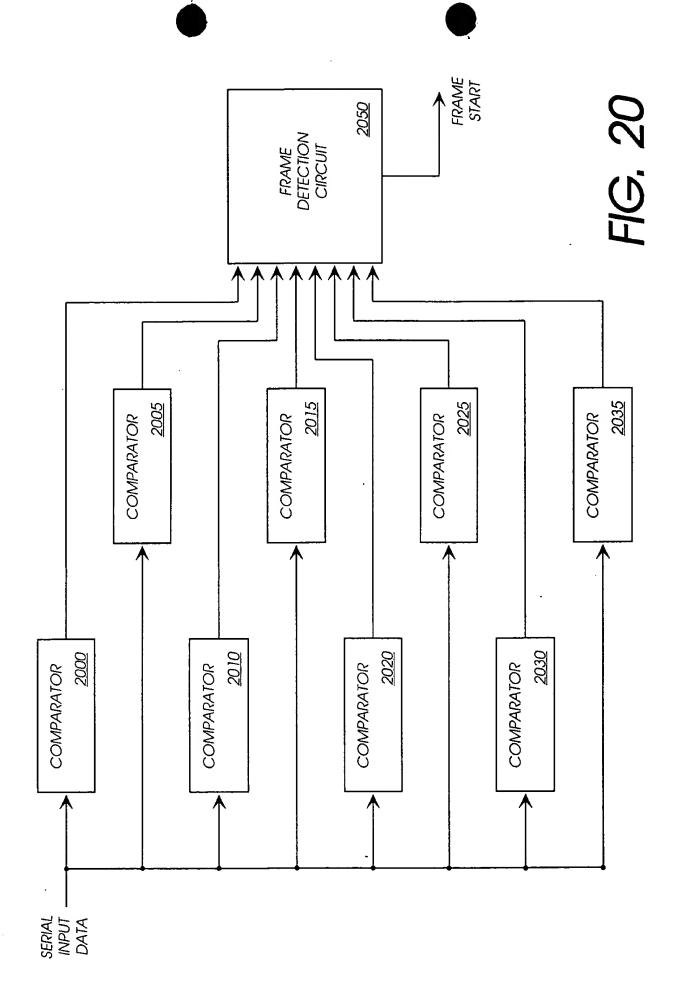


FIG. 19



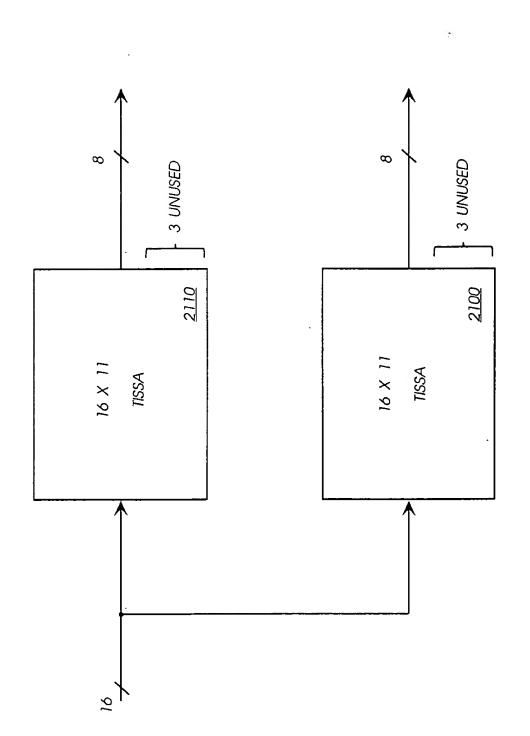
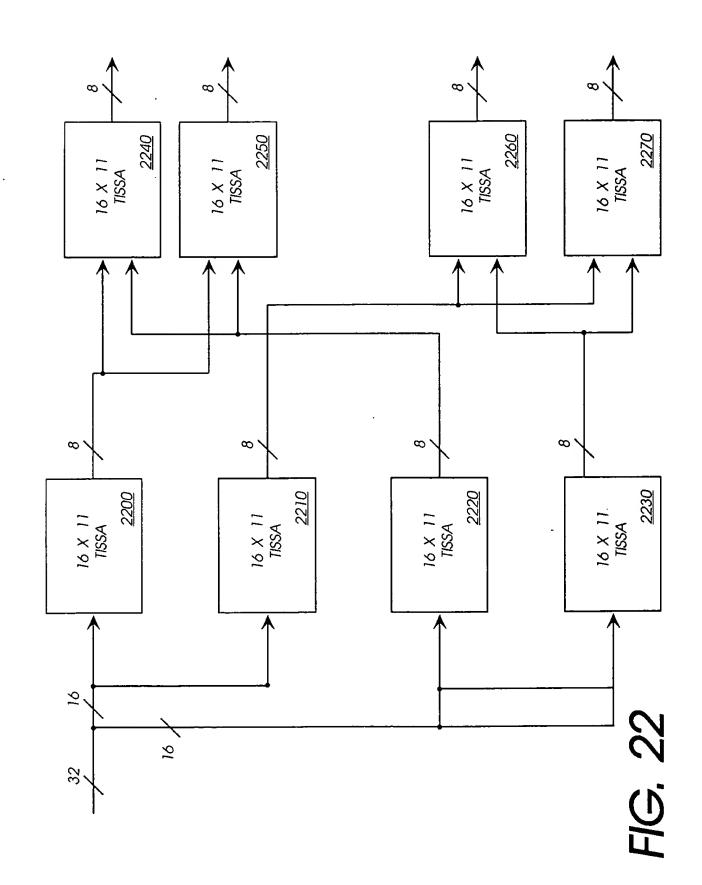


FIG. 21



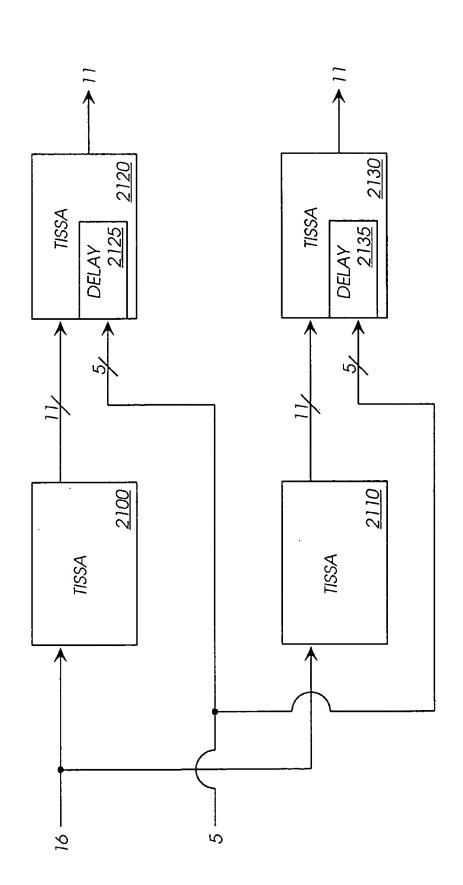


FIG. 23